HOGAN & HARTSON - 81788.0261
Go IWASAKI, "Output Buffer Circuit And Semiconductor Memory Using The Same"
Express Mail Label EV 325 214 875 US
11 Drawing Sheets; Sheet 1 of 11

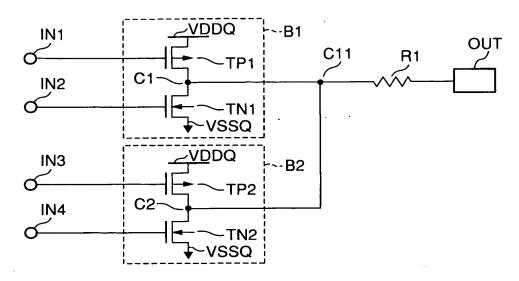


FIG.1

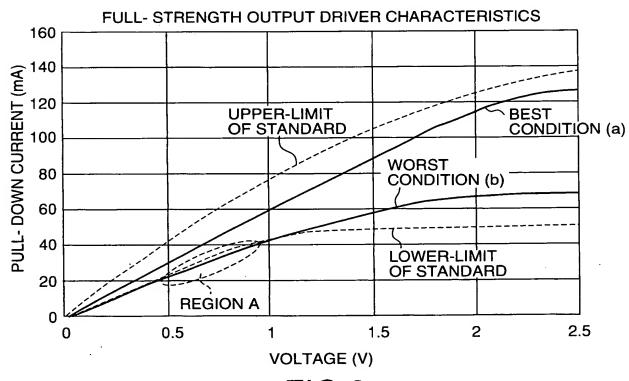


FIG.2

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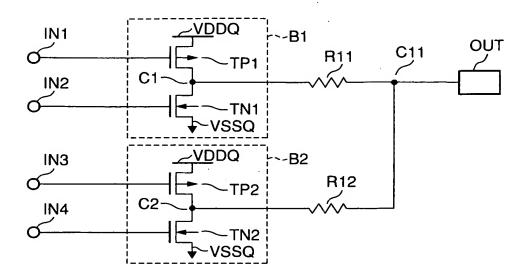
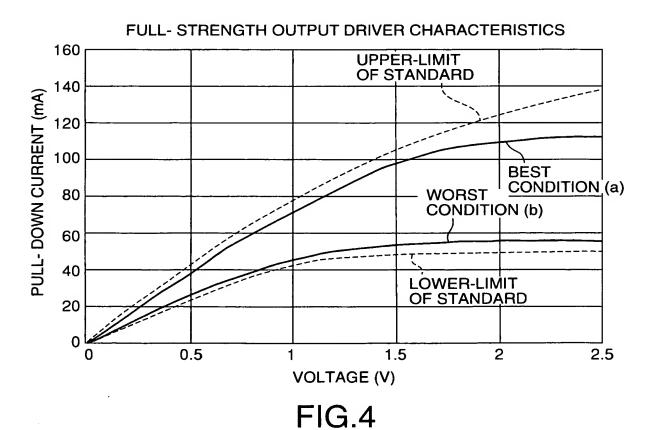


FIG.3



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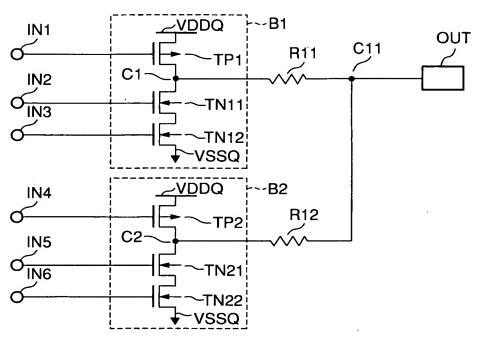
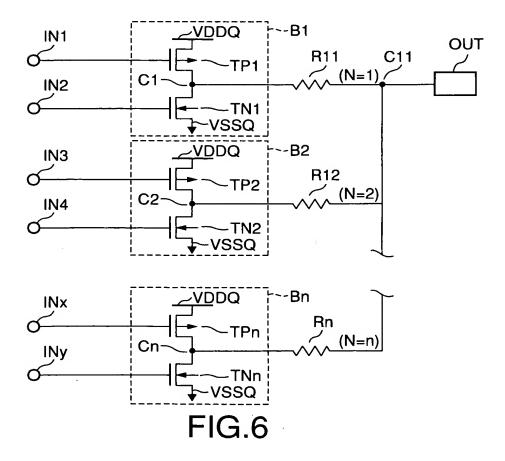
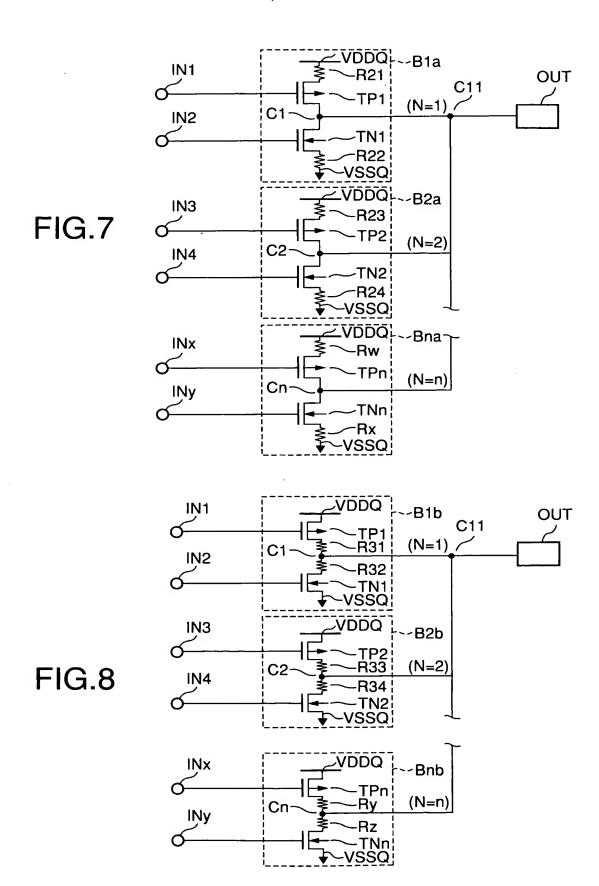


FIG.5



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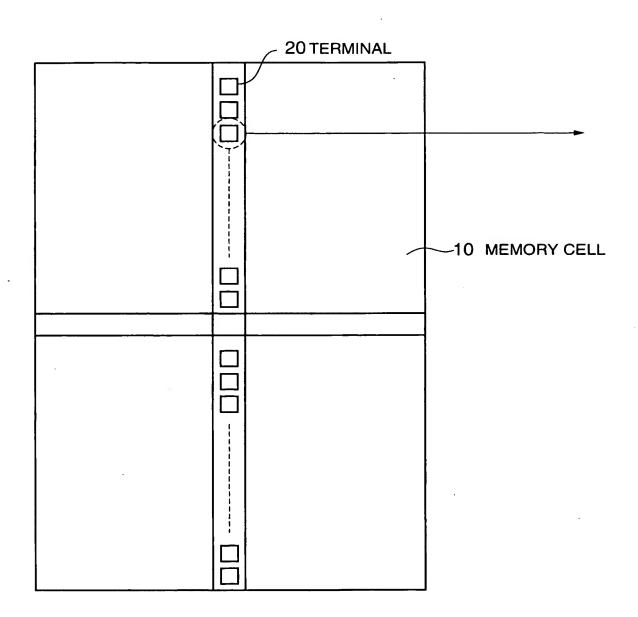


FIG.9

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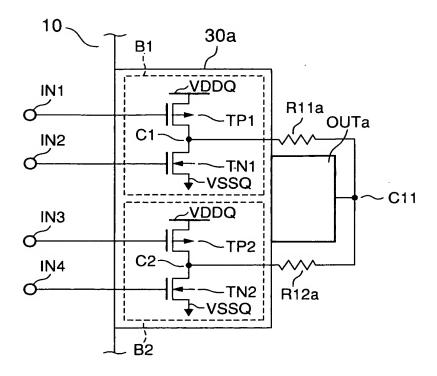
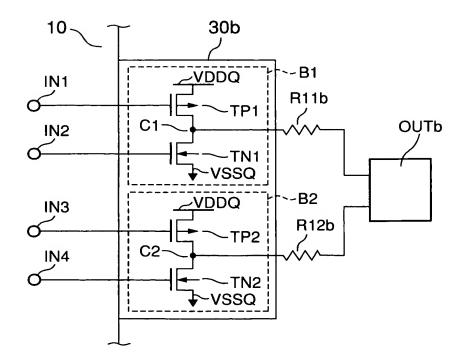
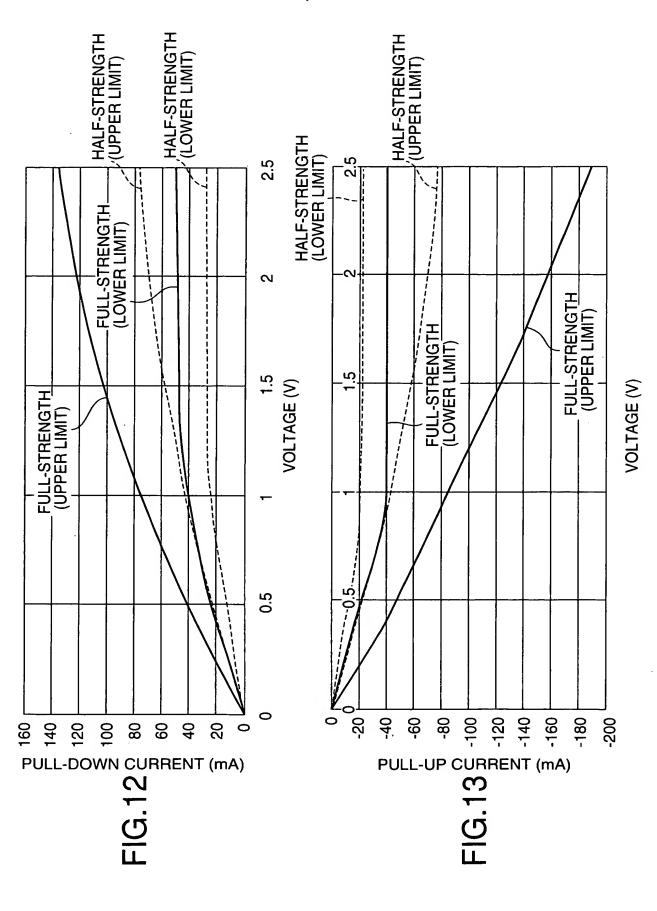


FIG.10



**FIG.11** 

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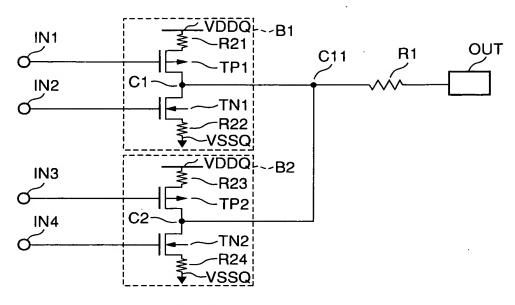


FIG.14

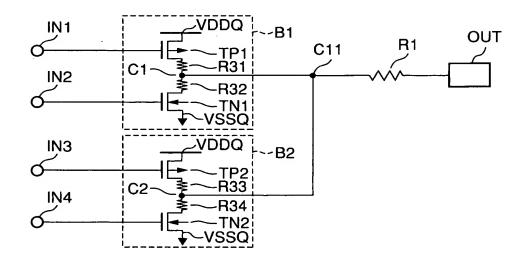


FIG.15

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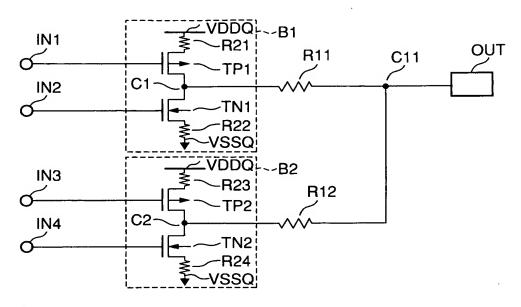


FIG.16

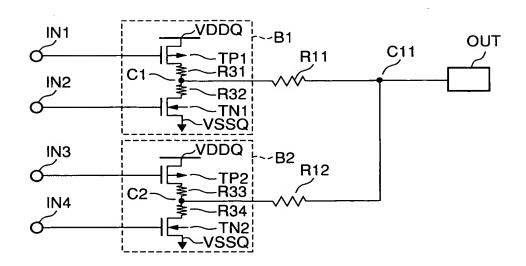


FIG.17

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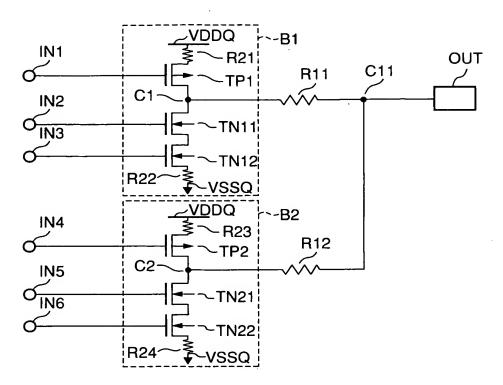


FIG.18

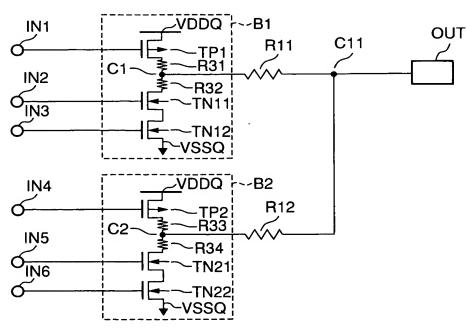


FIG.19

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Memory Using The Same"
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